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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as phecessary) OCT 1 3 2004						Filing Date		March 31, 2004	
						First Named Inventor:		Peter G. Tolchinsky	
						Art Unit	1765		
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Sheet	4	1 67 1				Attorney Docket Number	42P18279	42P18279	
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Examiner Initials*	Cite No <sup>1</sup>		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published						
n		pMOSF Technol	SAYAMA, H., ET AL., Effect of <100> Channel Direction for High Performance SCE Immune pMOSFET with Less Than 0.15µm Gate Length, ULSI Development Center, * Advanced Technology R & D Center, Mitsubishi Electric Corporation, 4-1 Mizuhara, Itami, Hyogo 664-8641, Japan, © 1999 IEEE, IEDM 99-657, pages 27.5.1 – 27.5.4.						
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